

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	281	(712/28).CCLS.	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 16:00
L2	432	(712/11).CCLS.	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 15:58
L3	80	((dynamic or reconfigurable) adj instruction adj set) and array	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 15:59
L4	1392	(709/213).CCLS.	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 16:01
L5	3088	(processor or microprocessor) with (tile or (building adj block))	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 16:02
L6	279	(processor or microprocessor) near2 (tile)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 16:06
L7	105	iWarp	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	AND	ON	2008/01/30 16:02